



SK22 THRU SK210

SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

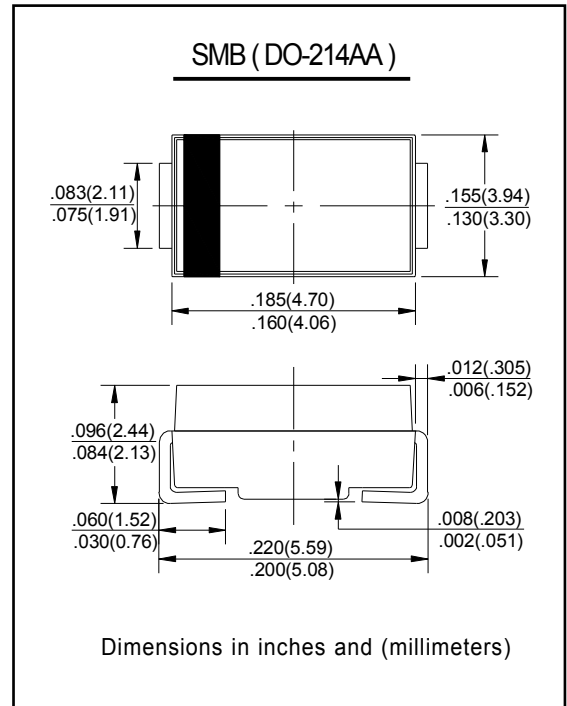
Reverse Voltage - 20 to 100 Volts Forward Current - 2.0 Ampere

FEATURES

- The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- For surface mounted applications
- Metal silicon junction, majority carrier conduction
- Low reverse leakage
- Built-in strain relief, ideal for automated placement
- High forward surge current capability
- High temperature soldering guaranteed: 250°C/10 seconds at terminals

MECHANICAL DATA

Case: JEDEC SMB(DO-214AA) molded plastic body
Terminals: Solder plated, solderable per MIL-STD-750, Method 2026
Polarity: Color band denotes cathode end
Mounting Position: Any
Weight: 0.005 ounce, 0.138 grams



Maximum Ratings and Electrical Characteristics @_{T_A}=25°C unless otherwise specified

Characteristic	Symbol	SK22	SK23	SK24	SK25	SK26	SK28	SK29	SK210	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	20	30	40	50	60	80	90	100	V
Working Peak Reverse Voltage	V _{RWM}									V
DC Blocking Voltage	V _R									V
RMS Reverse Voltage	V _{R(RMS)}	14	21	28	35	42	56	64	71	V
Average Rectified Output Current @ _{T_L} = 75°C	I _O	2.0								A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	50								A
Forward Voltage @ _{I_F} = 2.0A	V _{FM}	0.50			0.70		0.85			V
Peak Reverse Current @ _{T_A} = 25°C At Rated DC Blocking Voltage @ _{T_A} = 100°C	I _{RM}	0.5								mA
		20								
Typical Thermal Resistance (Note 1)	R _{θJL} R _{θJA}	17 75								°C/W
Operating Temperature Range	T _j	-65 to +125								°C
Storage Temperature Range	T _{STG}	-65 to +150								°C

Note: 1. Mounted on P.C. Board with 5.0mm² copper pad area.



SK22 THRU SK210

RATINGS AND CHARACTERISTIC CURVES

FIG. 1- FORWARD CURRENT DERATING CURVE

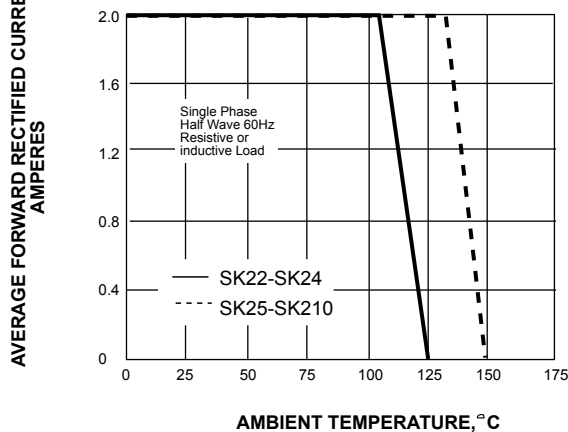


FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

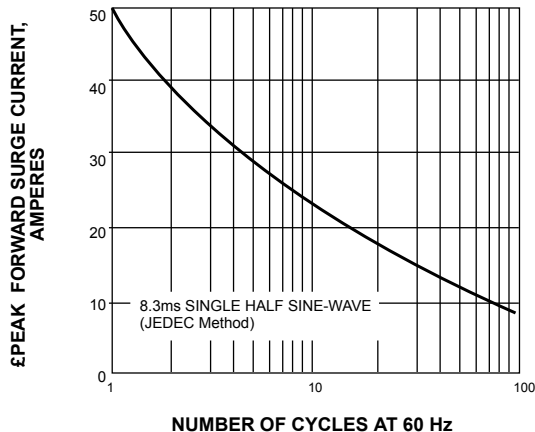


FIG. 3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

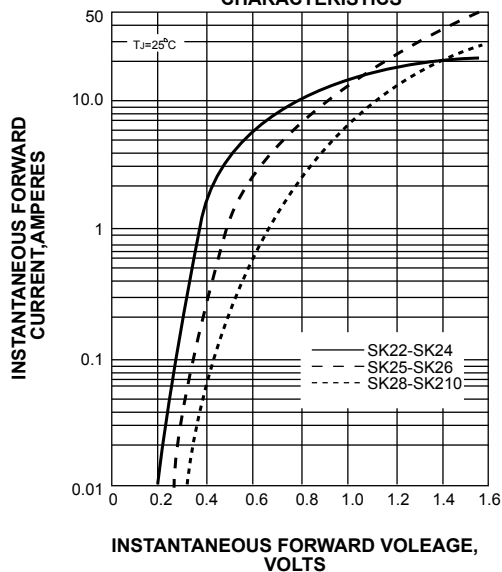


FIG. 4-TYPICAL REVERSE CHARACTERISTICS

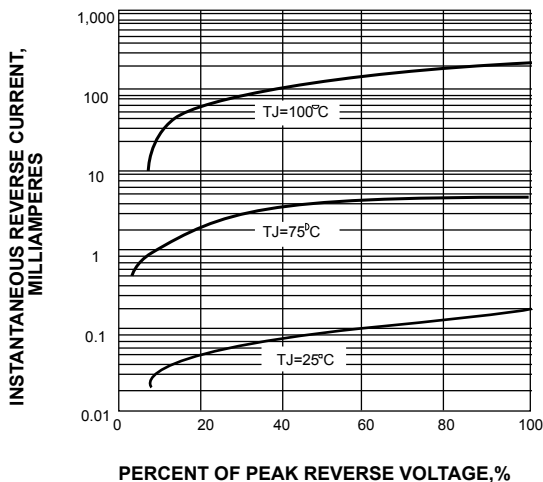


FIG. 5-TYPICAL JUNCTION CAPACITANCE

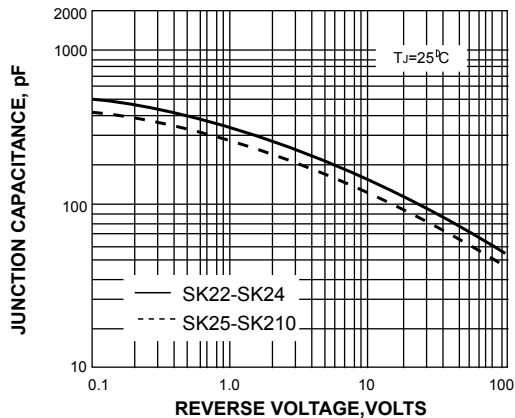


FIG. 6-TYPICAL TRANSIENT THERMAL IMPEDANCE

